CS636: Parallel Prefix Scan

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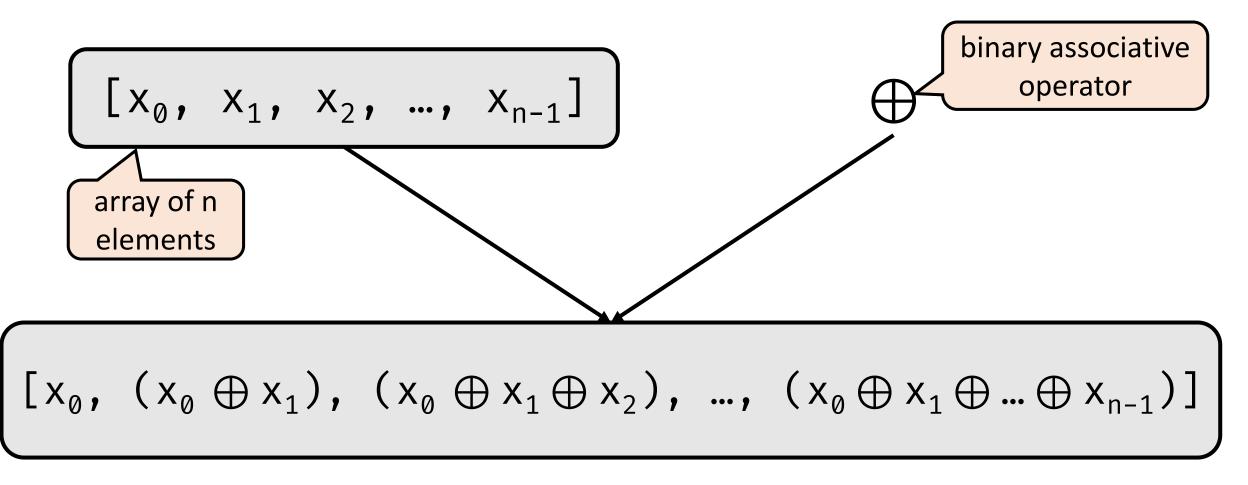
Semester 2018-2019-II CSE, IIT Kanpur

Content influenced by many excellent references, see References slide for acknowledgements.

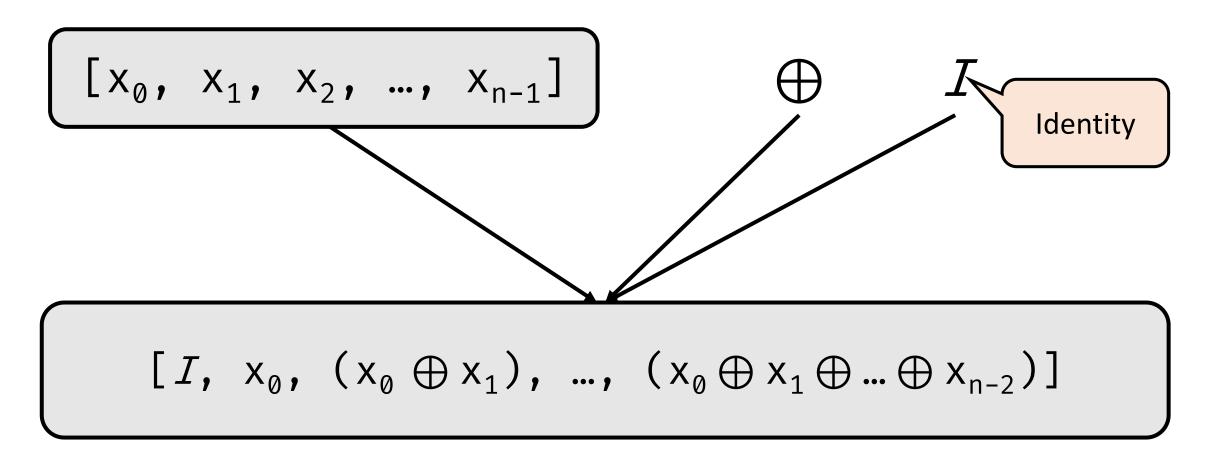
int arr
$$[8] = \{10, 1, 4, 2, 9, 5, 7, 8\}$$

int sum_arr[8] = $\{10, 11, 15, 17, 26, 31, 38, 46\}$

Definition of Inclusive Prefix Scan







A Problem

- Assume we have a 100-inch sandwich to feed ten people
- We know how many inches each person wants

- How do we cut the sandwich quickly and distribute?
- Method 1: Cut the sandwich sequentially starting from say left

Yong Cao. Parallel Prefix Sum – Scan, ECE 408/498AL, UIUC.

Method 2

• Calculate prefix sum and cut in **parallel**

Prefix Sum

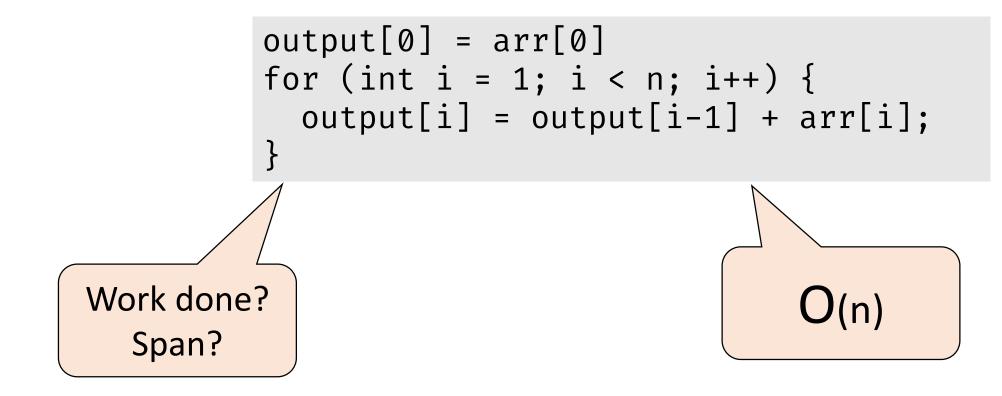
• Inclusive Sum

$$Output_i = \sum_{j=0}^i arr_j$$

• Exclusive Sum

$$Output[0] = 0 \land Output_i = \sum_{j=0}^{i-1} arr_j, i > 0$$

Sequential Inclusive Prefix Sum



Analysis of Parallel Algorithms

- T_p = Execution time of a parallel program with p processors
- Work
 - Total number of computation operations performed by the p processors
 - Time to run on a single processor (T_1)
- Span
 - Length of the longest series of sequential operations or the critical path
 - Time taken to run on infinite processors (T_{∞})

Analysis of Parallel Algorithms

• Cost

• Total time spent by **all** processors in computation (pT_p)

\bigcap	$Cost \ge Work$	
	$pT_p \ge T_1$	

Execution time \geq Span $T_n \ge T_\infty$

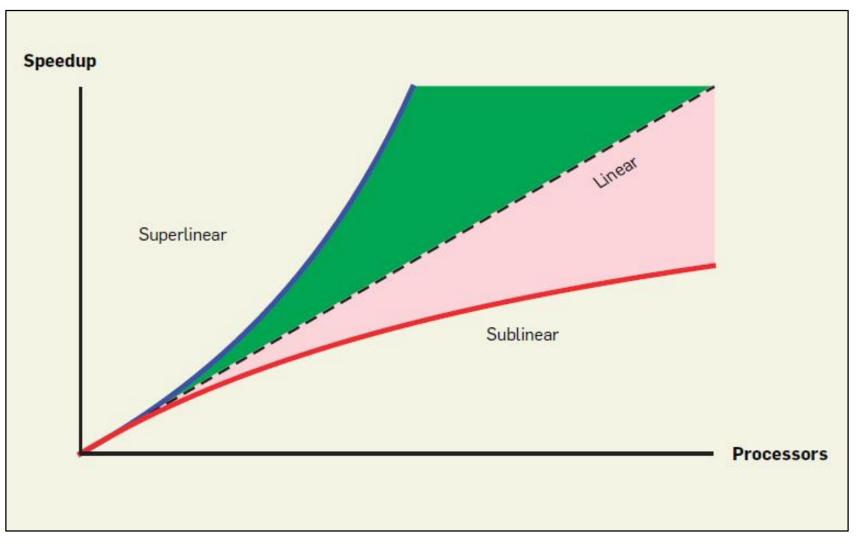
Analysis of Parallel Algorithms

• Speedup (S_p)

• Total time spent by all processors in computation (pT_p)

Speedup =
$$\frac{T_1}{T_p} \le p$$

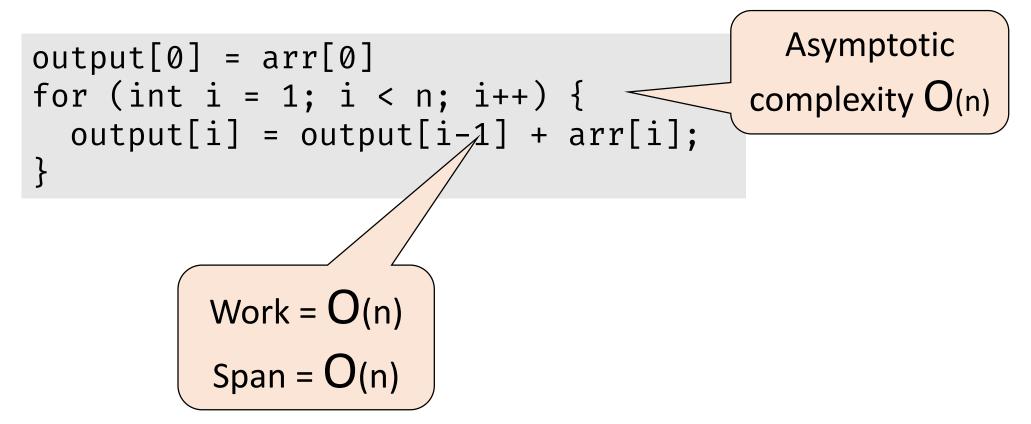
Speedup



Other Metrics

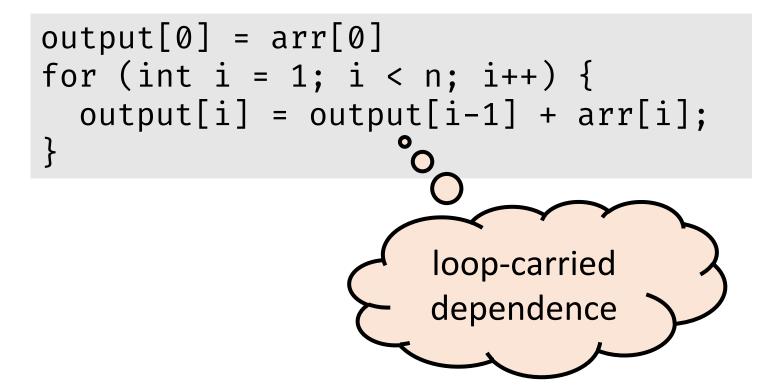
- Efficiency
 - Speedup per processor $\frac{S_p}{n}$
- Parallelism
 - Maximum possible speedup given any number of processors $\frac{T_1}{T_{\infty}}$

Sequential Inclusive Prefix Scan



Parallel Prefix Sum

How can Inclusive Prefix Scan be Parallelized?



A Naïve Parallel Prefix Sum

- Use one thread to compute each output element
 - The thread adds up all the previous elements needed for the output

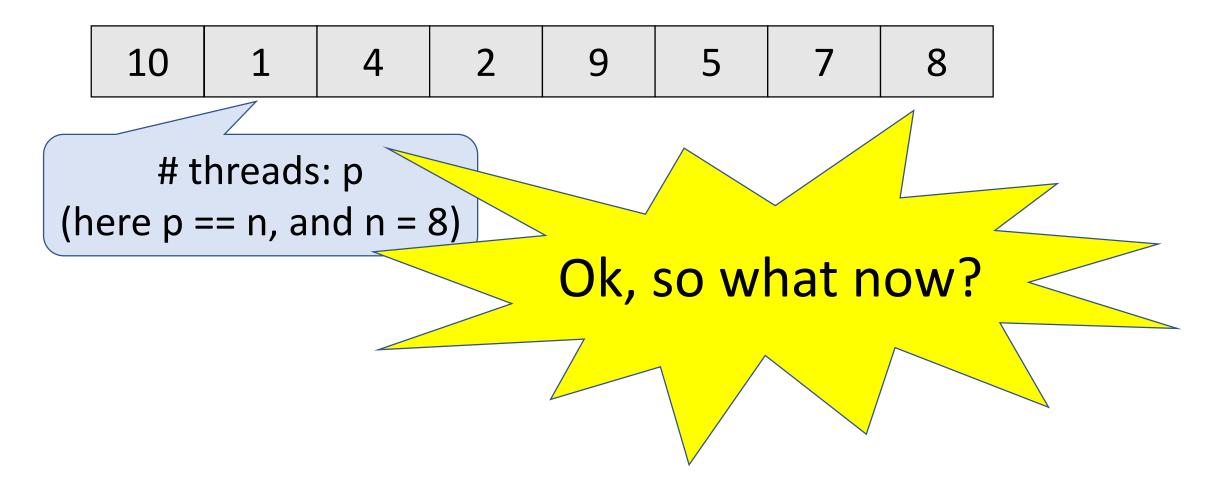
$$y_0 = x_0$$

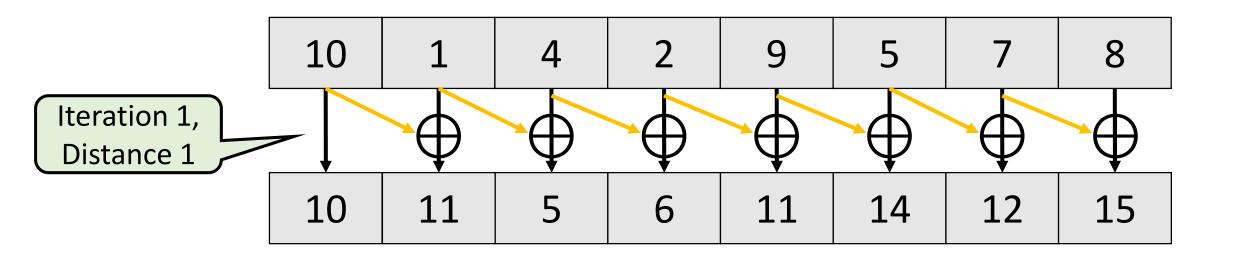
 $y_1 = x_0 + x_1$
 $y_2 = x_0 + x_1 + x_2$

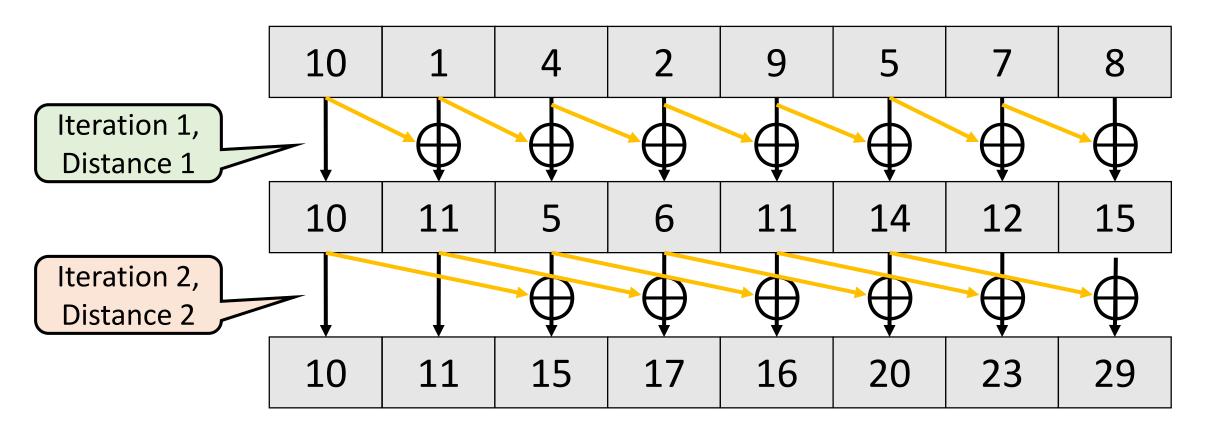
• Work =
$$1 + 2 + 3 + ... + n = \frac{n(n+1)}{2}$$

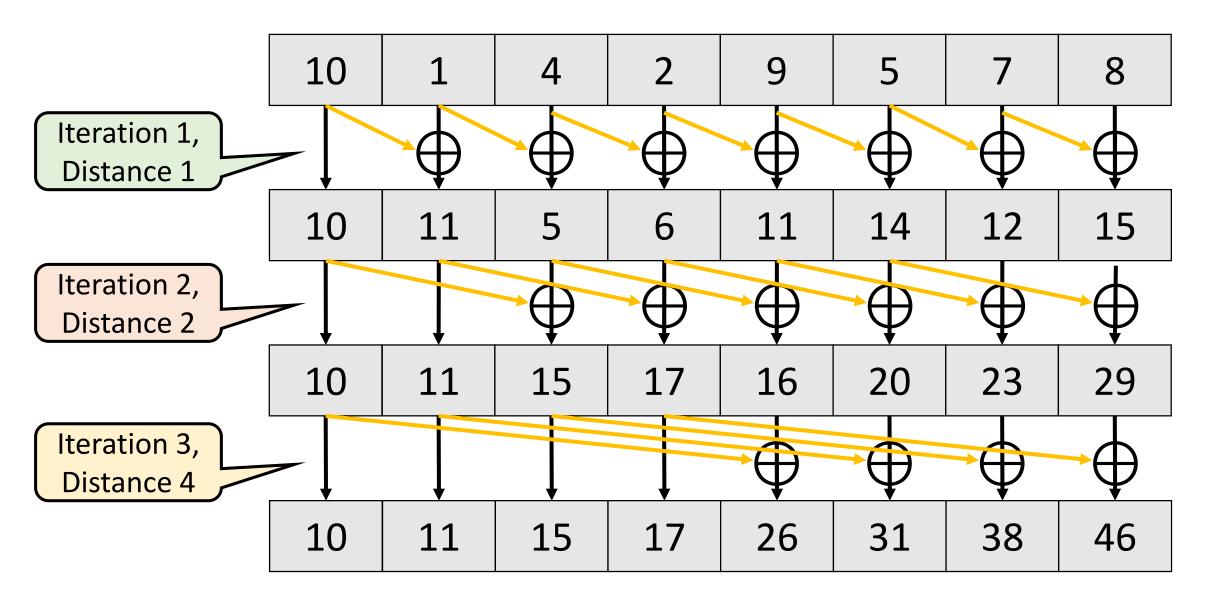
= $O(n^2)$ operations

Parallel Inclusive Prefix Sum









Algorithm Efficiency

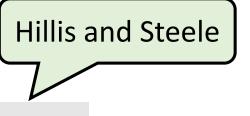
- # of iterations: log n
- First iteration: (n-1) additions
- Second iteration: (n-2) additions
- Third iteration: (n-4) additions
- Last iteration: (n n/2) additions

• Total additions =
$$(n - 1) + (n - 2) + (n - 4) + \dots + \left(n - \frac{n}{2}\right)$$

= $n \log n - \left(1 + 2 + 4 + \dots + \frac{n}{2}\right)$
= $n \log n - (n - 1) = O(n \log n)$

Algorithm Efficiency

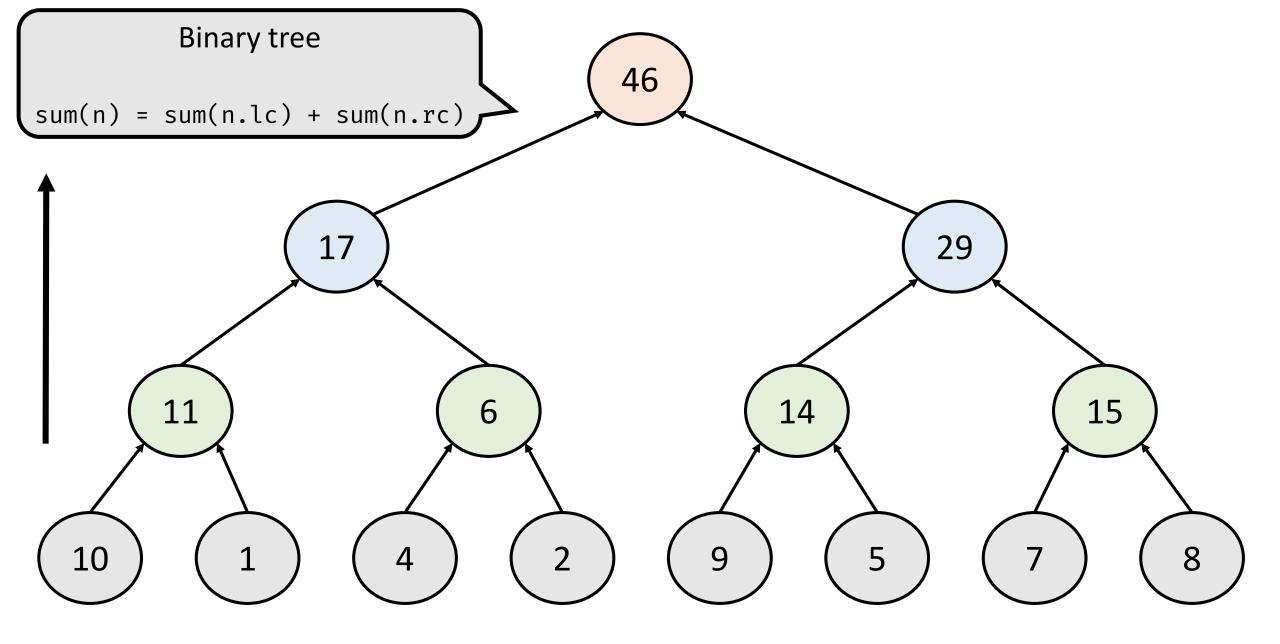
- Work = $O(n \log n)$
 - Remember Work for the sequential algorithm was O(n)
 - For large *n*, log *n* can be a non-trivial factor

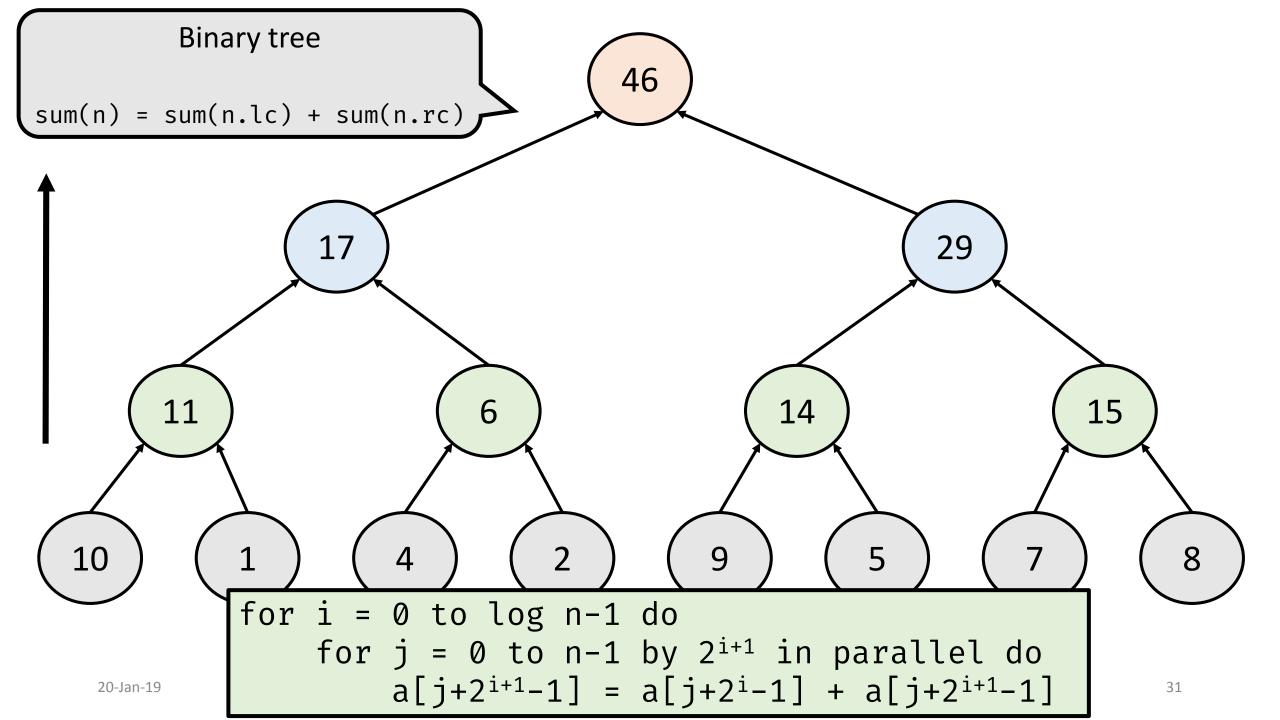


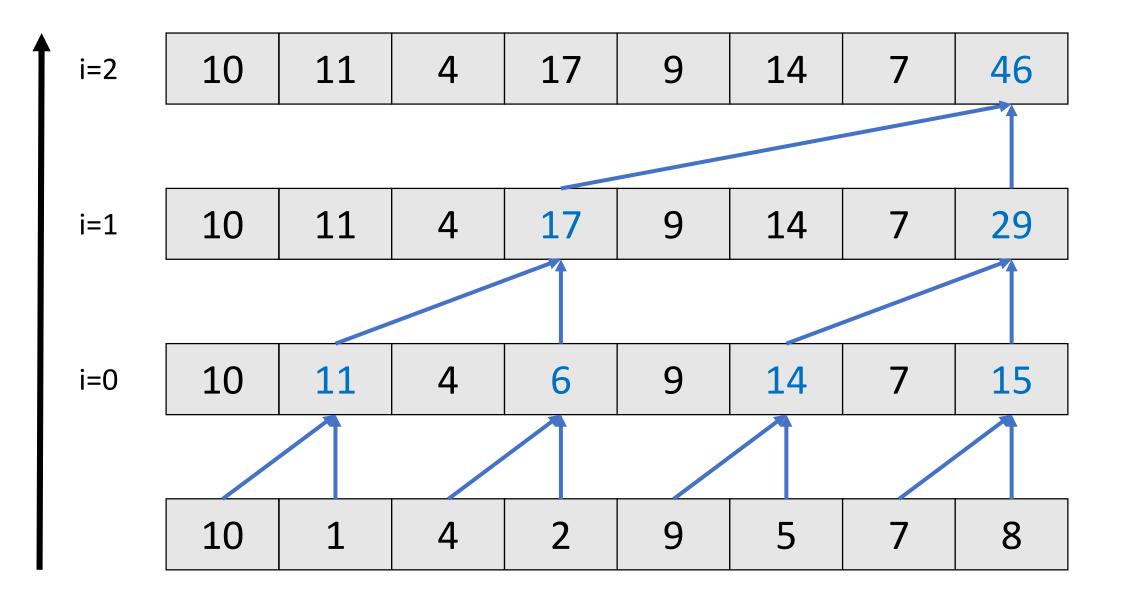
for
$$i = 0$$
 to $\lceil \log n - 1 \rceil$ do
for $j = 2^{i}$ to $n - 1$ **in parallel** do
 $A [j] = A[j] + A[j - 2^{i}]$
complexity $O(\log n)$

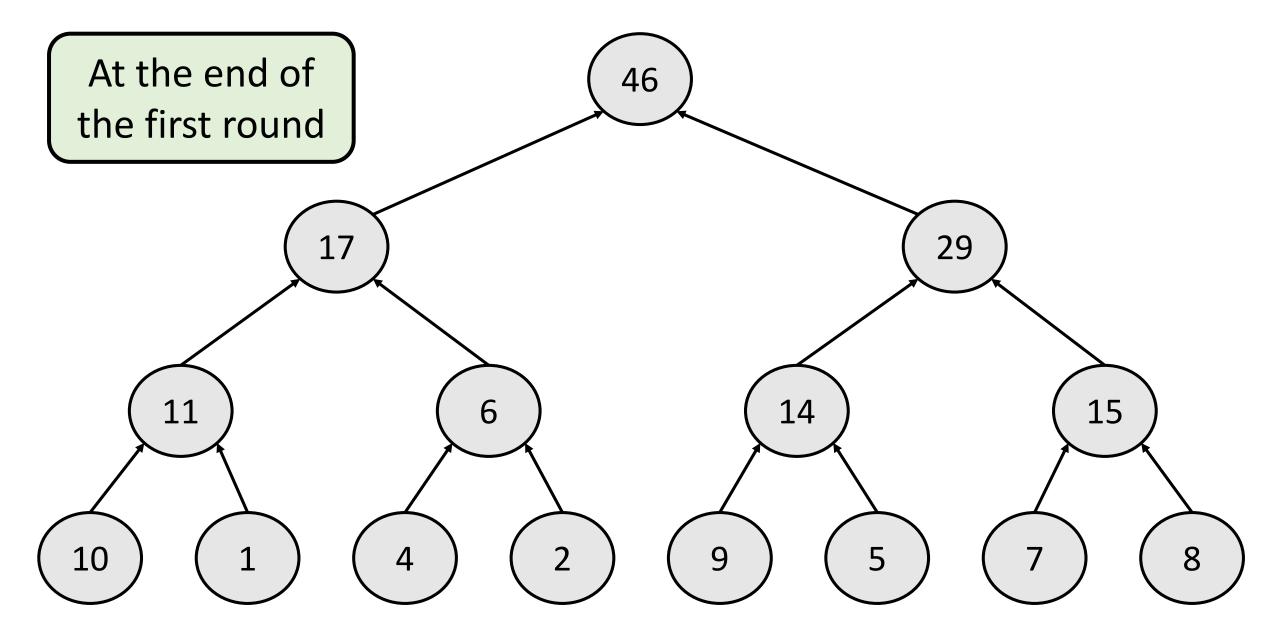
Algorithm With Improved Work-Efficiency

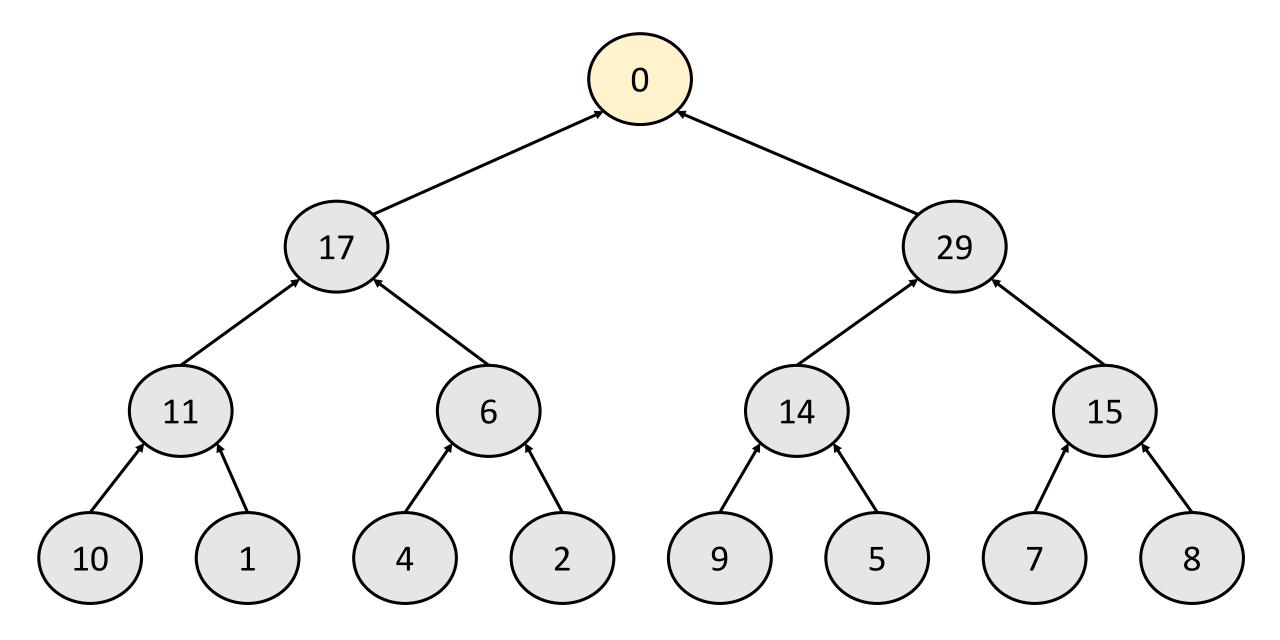
	10	1	4	2	9	5	7	8
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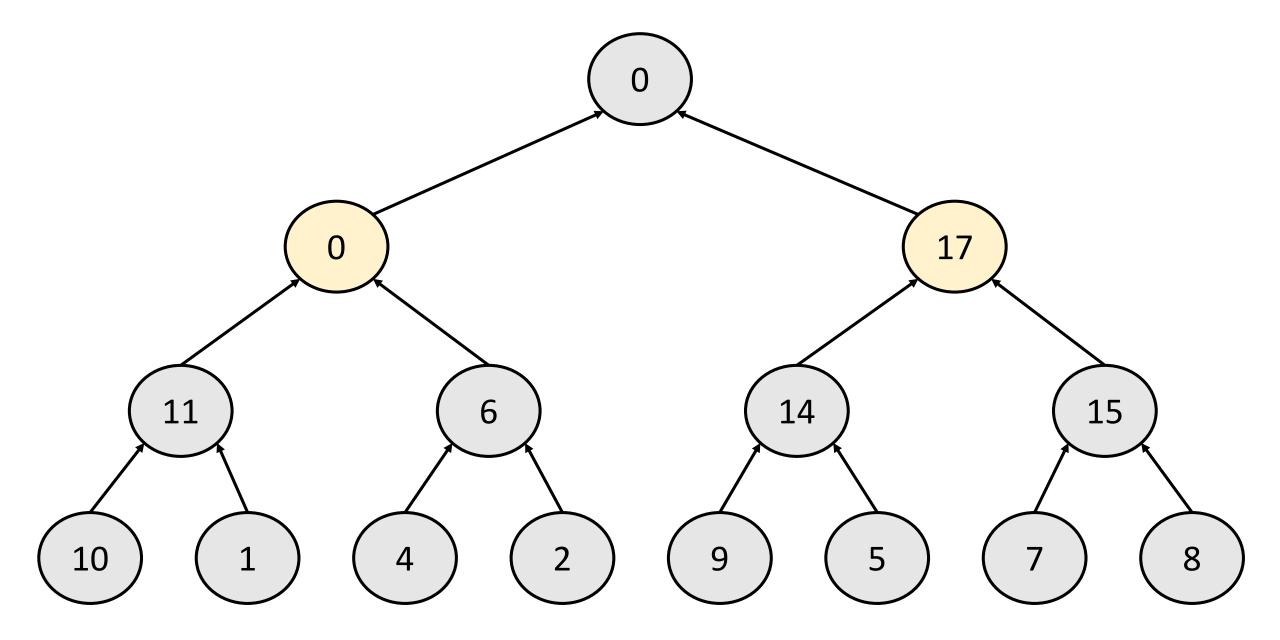


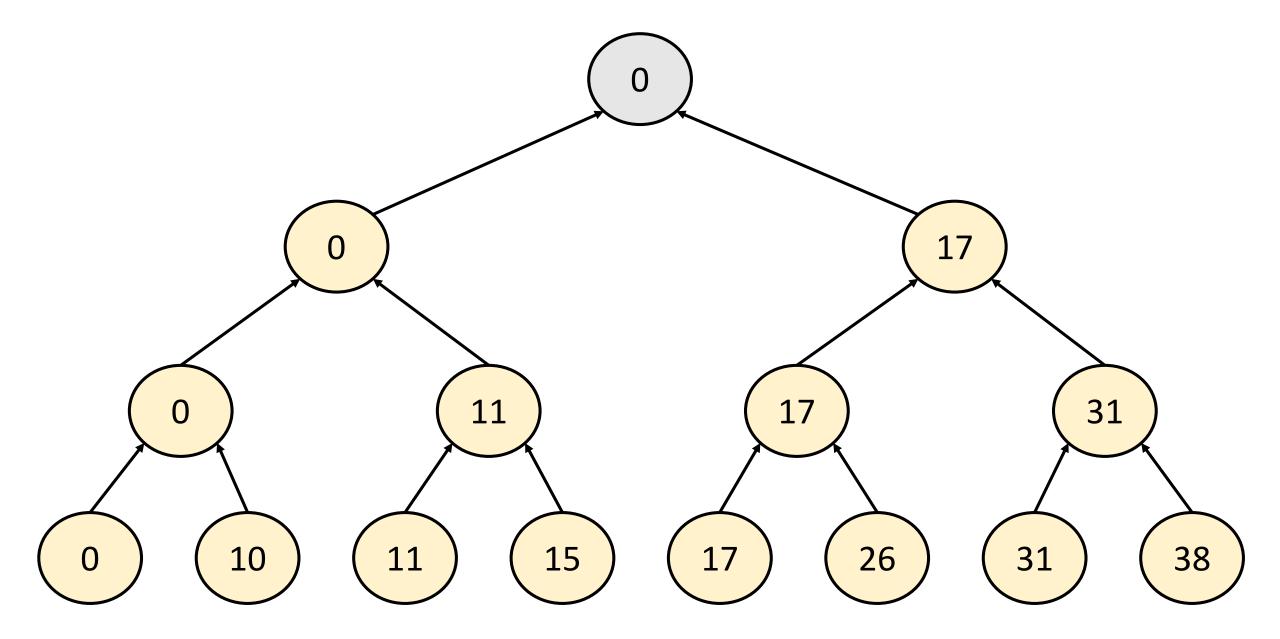


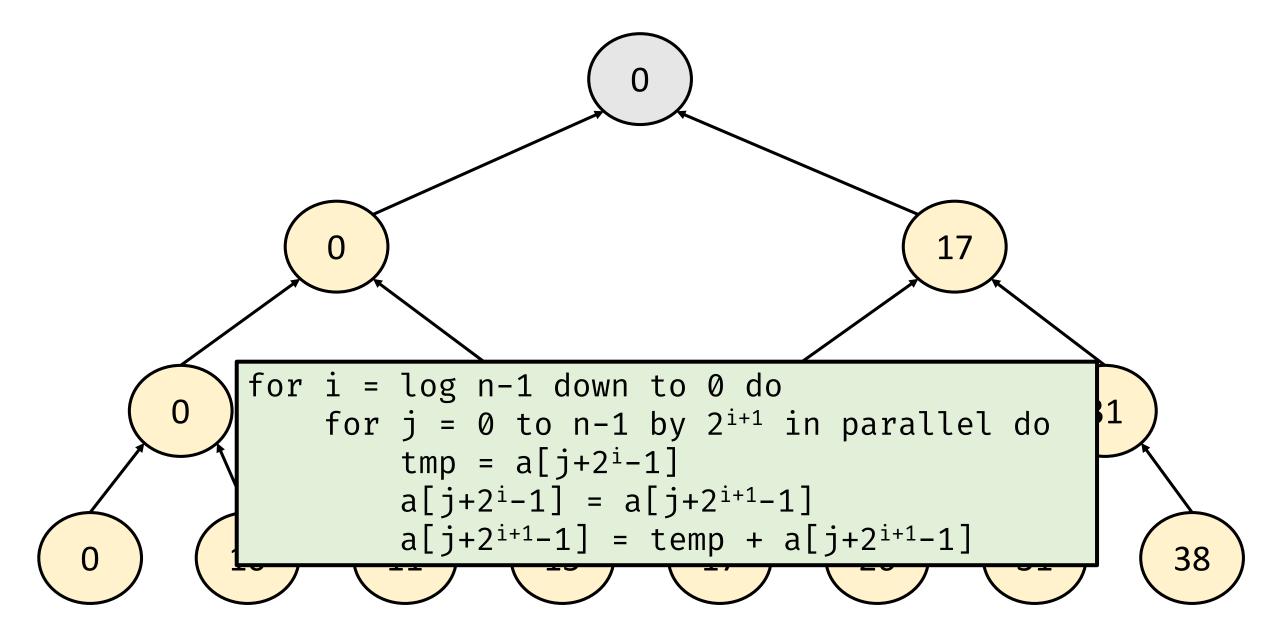


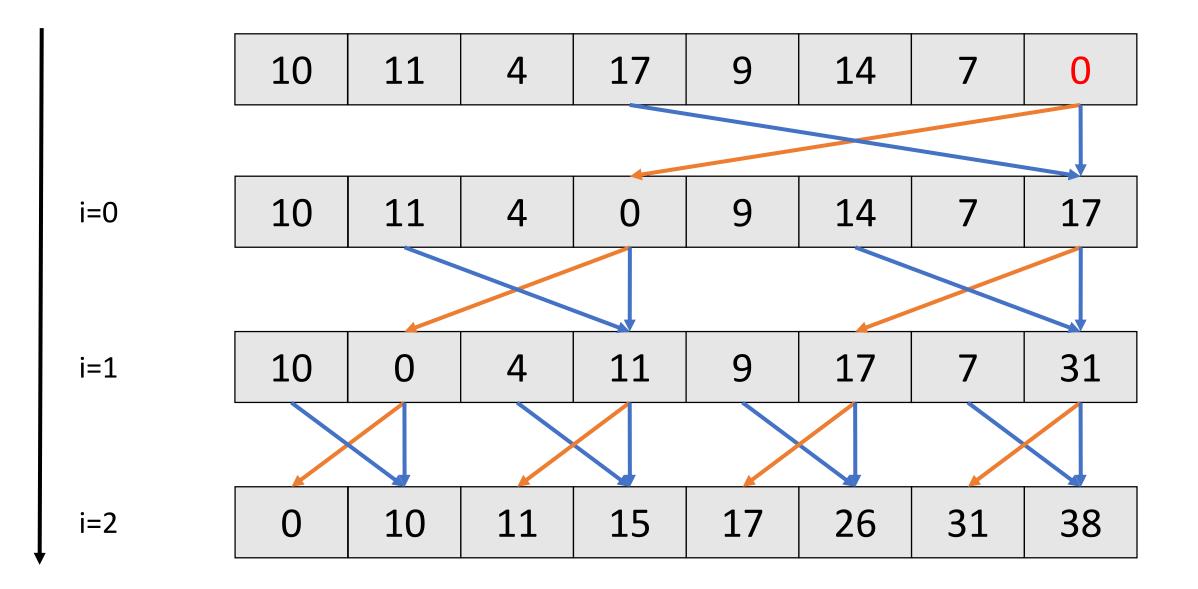


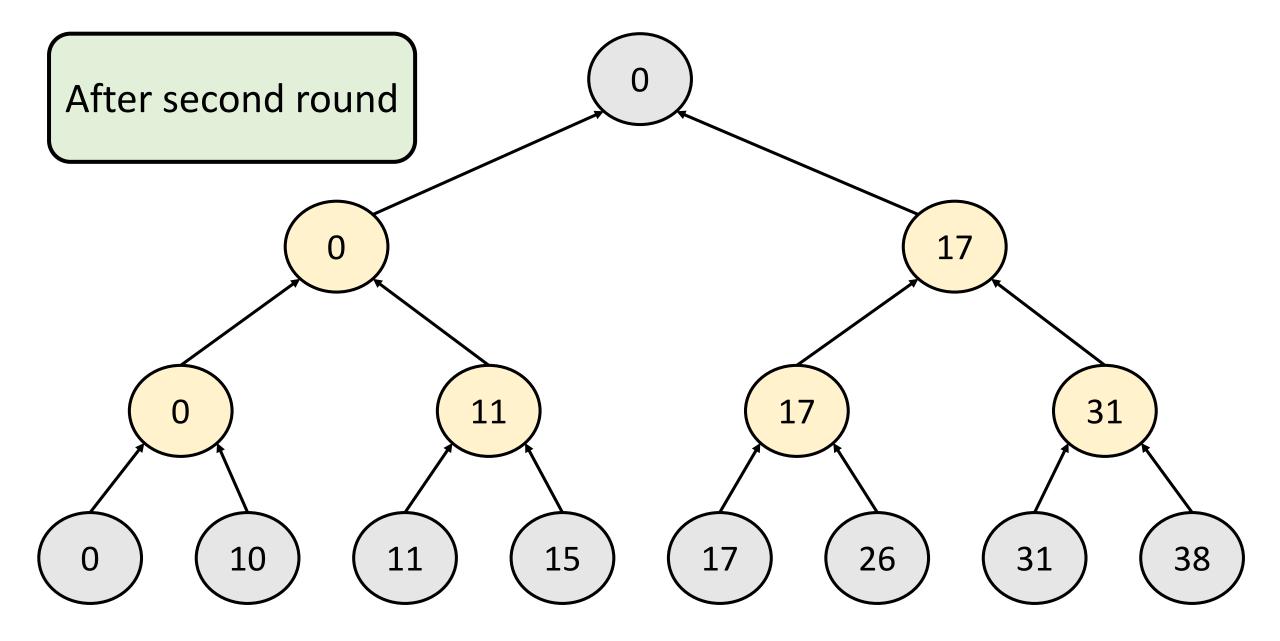




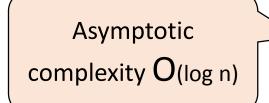








Algorithm Efficiency



Algorithm Efficiency

- # of iterations: log n in each pass
- Number of addition operations in first pass: $\frac{n}{2} + \frac{n}{4} + \dots + 2 + 1$
- Number of addition operations in second pass: $1 + 2 + \dots + \frac{n}{2}$
- Total additions = (n 1) + (n 1) = 2(n 1)= O(n)

Benefits from parallelism can overcome the constant factor increase in computation

References

- Yong Cao. Parallel Prefix Sum Scan.
- G. Blelloch. Prefix Sums and Their Applications.
- Th. Ottmann. Parallel Prefix Computation.